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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
09/460,742 21186	12/14/1999 7590 07/23/2002	RAJENDRAN NAIR	[884.229US1	2896	
P.O. BOX 293	•	OESSNER & KLUTH, P.A.	EXAMINER TRA, ANH QUAN		
			ART UNIT	PAPER NUMBER	
			2816		
			DATE MAILED: 07/23/2002		

Please find below and/or attached an Office communication concerning this application or proceeding.

			.01.			
	Application No.	Applicant(s)				
Advisory Action	09/460,742	NAIR ET AL.				
	Examiner	Art Unit				
	Quan Tra	2816				
The MAILING DATE of this communication app	ars on the cover sheet with the c	orrespondence add	ress			
THE REPLY FILED 01 July 2002 FAILS TO PLACE THI Therefore, further action by the applicant is required to a final rejection under 37 CFR 1.113 may only be either: (1 condition for allowance; (2) a timely filed Notice of Appears amination (RCE) in compliance with 37 CFR 1.114.	void abandonment of this applice i) a timely filed amendment whi	cation. A proper rep ch places the applic	oly to a cation in			
PERIOD FOR RE	PLY [check either a) or b)]					
a) The period for reply expires <u>3</u> months from the mailing date of						
b) The period for reply expires on: (1) the mailing date of this Adv event, however, will the statutory period for reply expire later the ONLY CHECK THIS BOX WHEN THE FIRST REPLY WAS 706.07(f).	an SIX MONTHS from the mailing date of FILED WITHIN TWO MONTHS OF THE	the final rejection. EFINAL REJECTION. S	See MPEP			
Extensions of time may be obtained under 37 CFR 1.136(a). The data have been filed is the date for purposes of determining the period of extens of CFR 1.17(a) is calculated from: (1) the expiration date of the shortened b) above, if checked. Any reply received by the Office later than three most patent term adjustment. See 37 CFR 1.704(b).	sion and the corresponding amount of the statutory period for reply originally set in	fee. The appropriate ext	ension fee under (2) as set forth in			
 A Notice of Appeal was filed on Appellant's 37 CFR 1.192(a), or any extension thereof (37 CF 	R 1.191(d)), to avoid dismissal (period set forth in of the appeal.				
2. The proposed amendment(s) will not be entered be	ecause:					
(a) Ithey raise new issues that would require further consideration and/or search (see NOTE below);						
(b) ☐ they raise the issue of new matter (see Note below);						
(c) they are not deemed to place the application in better form for appeal by materially reducing or simplifying the issues for appeal; and/or						
(d) they present additional claims without cancel	ing a corresponding number of	finally rejected clair	ns.			
NOTE: See Continuation Sheet.						
 Applicant's reply has overcome the following reject 	tion(s):					
4. Newly proposed or amended claim(s) would canceling the non-allowable claim(s).	be allowable if submitted in a s	eparate, timely filed	d amendment			
5. ☐ The a) ☐ affidavit, b) ☐ exhibit, or c) ☐ request fo application in condition for allowance because:	r reconsideration has been cons	sidered but does NC	OT place the			
 The affidavit or exhibit will NOT be considered bed raised by the Examiner in the final rejection. 	cause it is not directed SOLELY	to issues which we	re newly			
7. For purposes of Appeal, the proposed amendment explanation of how the new or amended claims we	(s) a)⊠ will not be entered or b ould be rejected is provided belo) will be entered ow or appended.	and an			
The status of the claim(s) is (or will be) as follows:						
Claim(s) allowed:						
Claim(s) objected to:						
Claim(s) rejected: 4-6, 9, 10 and 14-16.						
Claim(s) withdrawn from consideration:						
8. The proposed drawing correction filed on is	a) ☐ approved or b) ☐ disapp	proved by the Exam	iner.			
9. Note the attached Information Disclosure Stateme						
0.☑ Other: <u>See Continuation Sheet</u>	,	Terry D. Cu Primary E	nningham xaminer			
Patent and Trademark Office						

Continuation Sheet (PTO-303) p09/460,742

Application No.

Continuation of 2. NOTE: the added limitations "the transistor operating in the depletion mode", "has a variable capacitance characteristic" in claim 1 and the limitation "having a variable capacitance characteristic" in claim 14 require further search and consideration.

Continuation of 10. Other: ragarding to the argument of claims 9-10 and 14-16, figure 6A shows a transistor having source and drain connected to VR and gate connected to Vo. .